

28940-117
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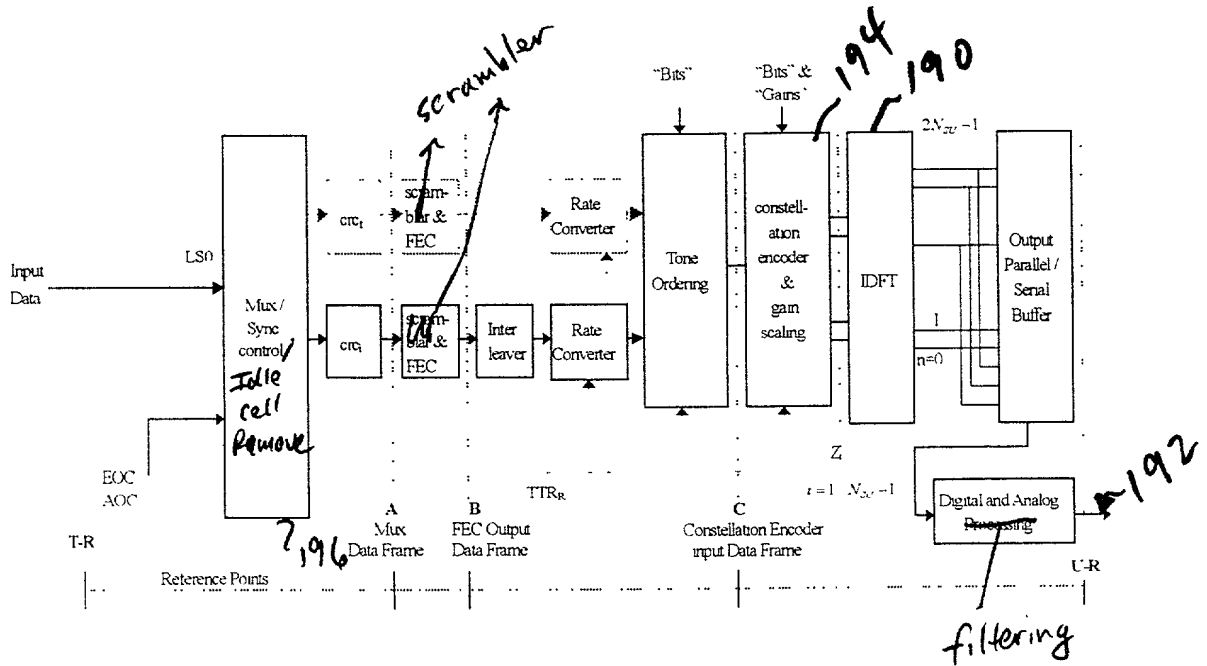


FIGURE 1 ~~ATU-R TRANSMITTER MODEL~~

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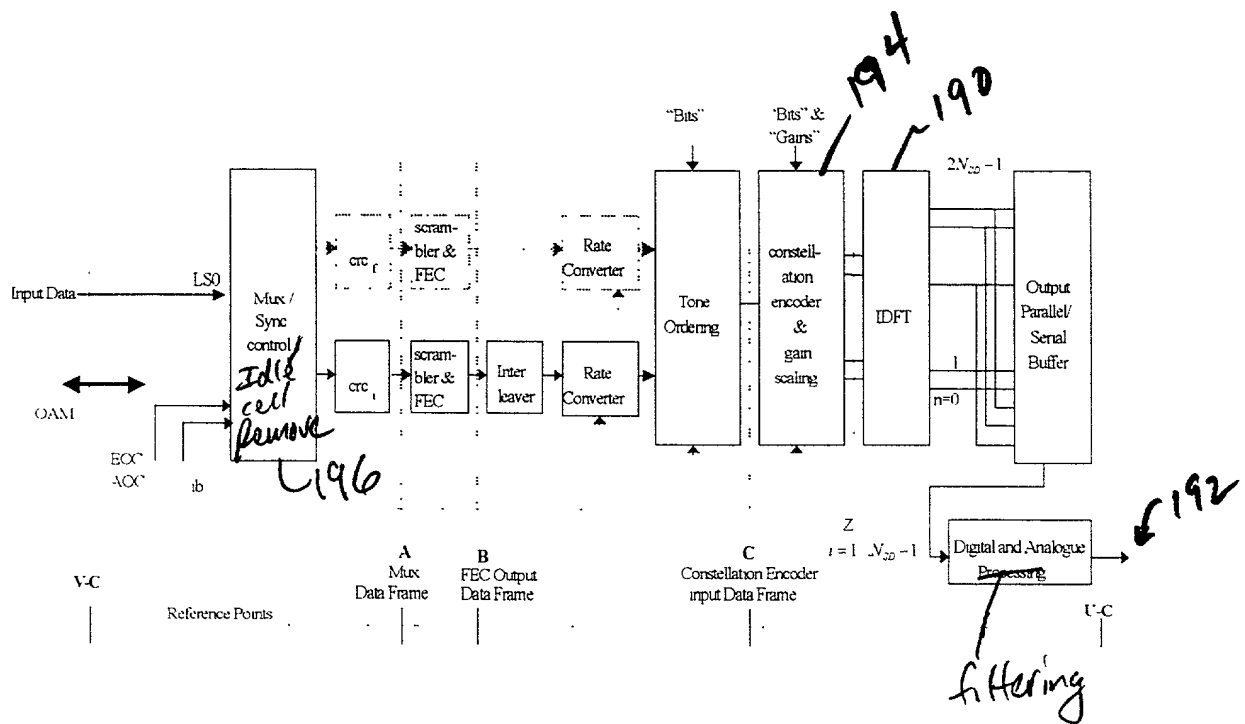


FIGURE 2 ~~ATU-C TRANSMITTER MODEL~~

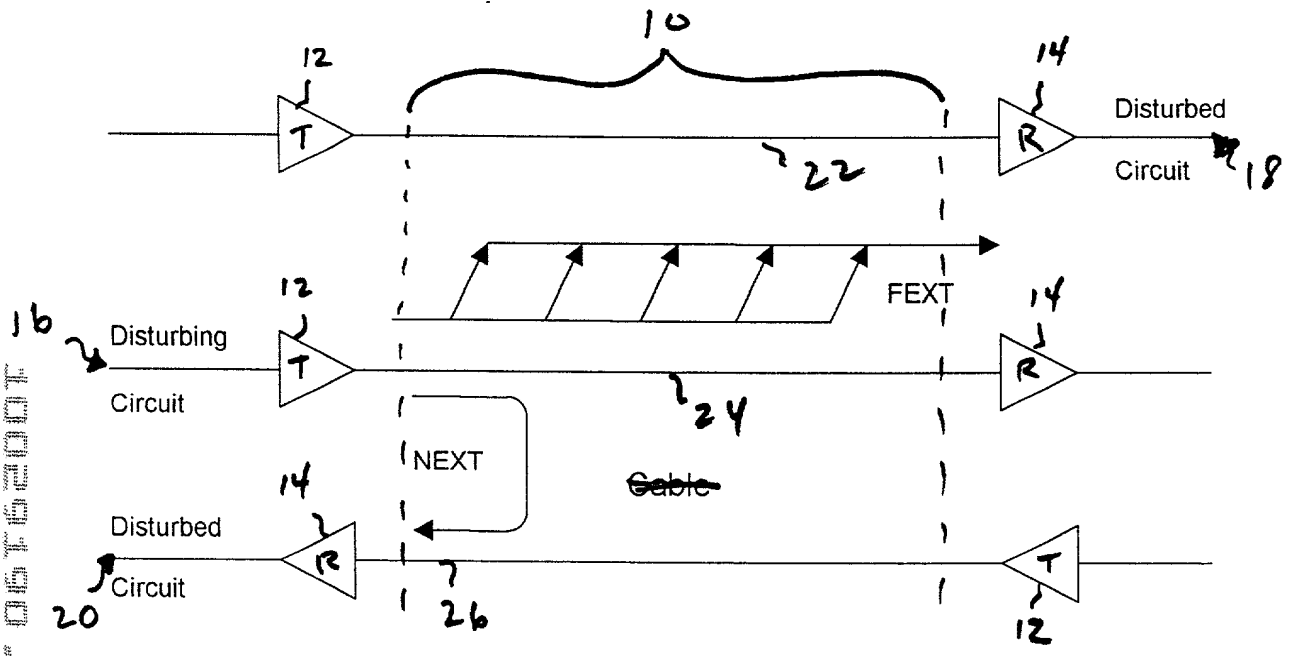
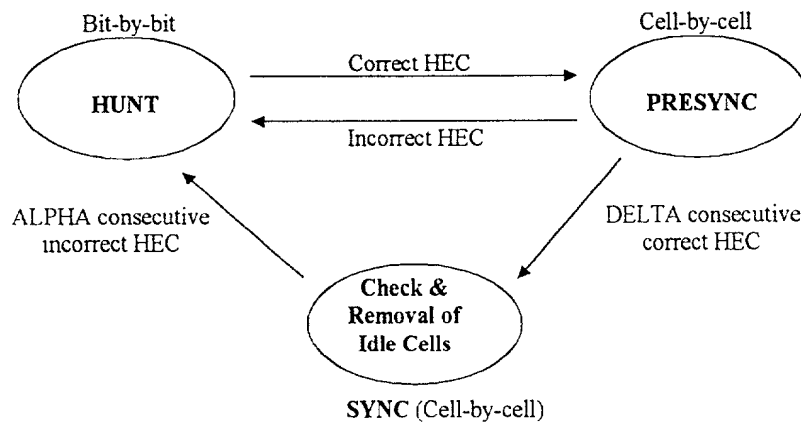


FIGURE 3. FAR-END CROSSTALK AND NEAR-END CROSSTALK COUPLING IN TYPICAL SUBSCRIBER LOOPS

FIGURE 3. FAR-END CROSSTALK AND NEAR-END CROSSTALK COUPLING IN TYPICAL SUBSCRIBER LOOPS

FOUO "06162001



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FIGURE 4 ATM IDLE CELLS REMOVAL STATE MACHINE

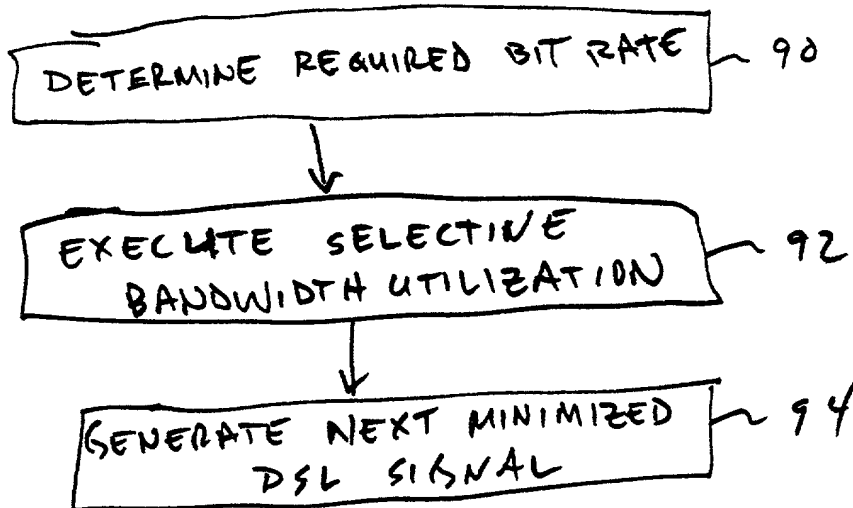


FIGURE 4

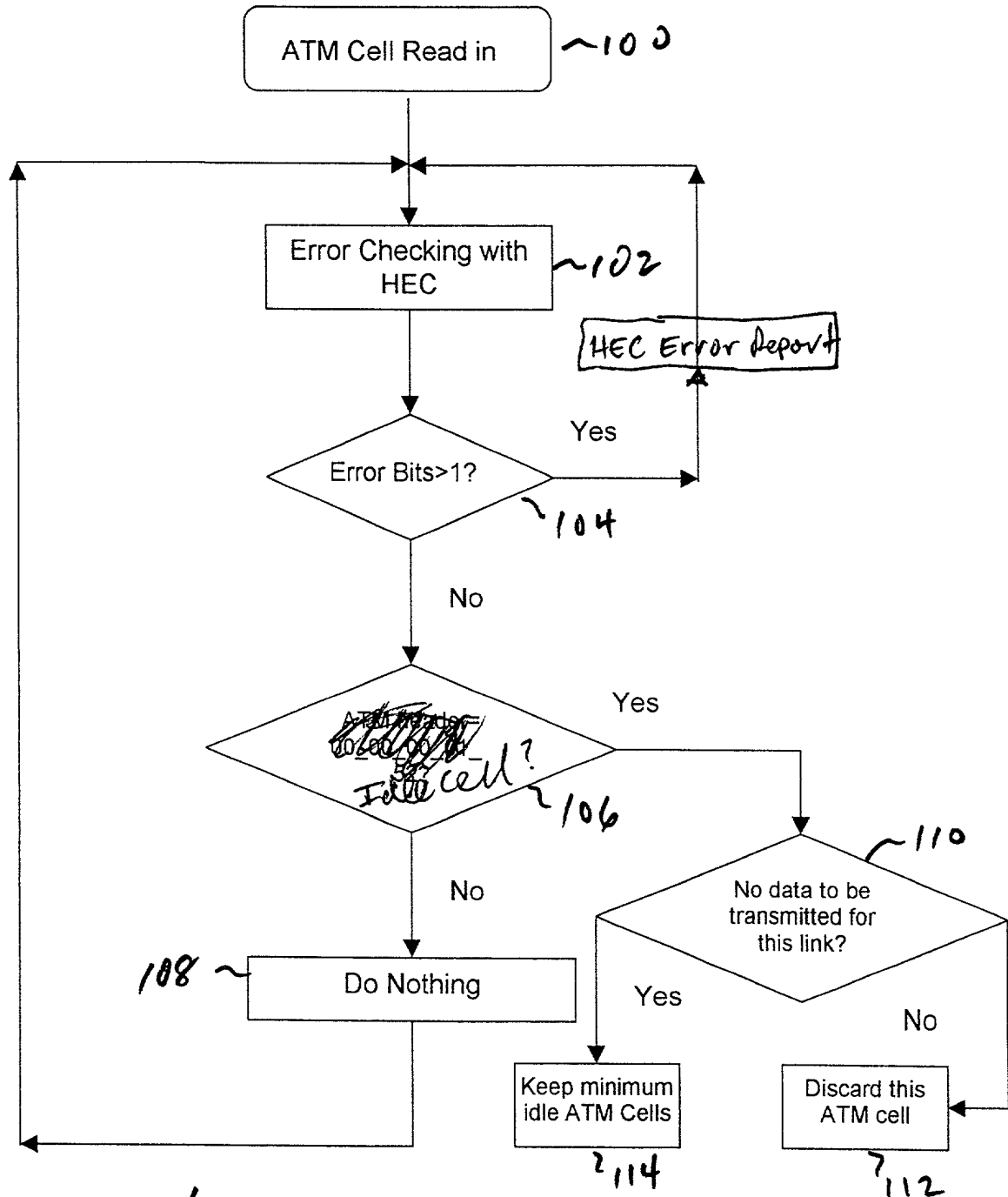
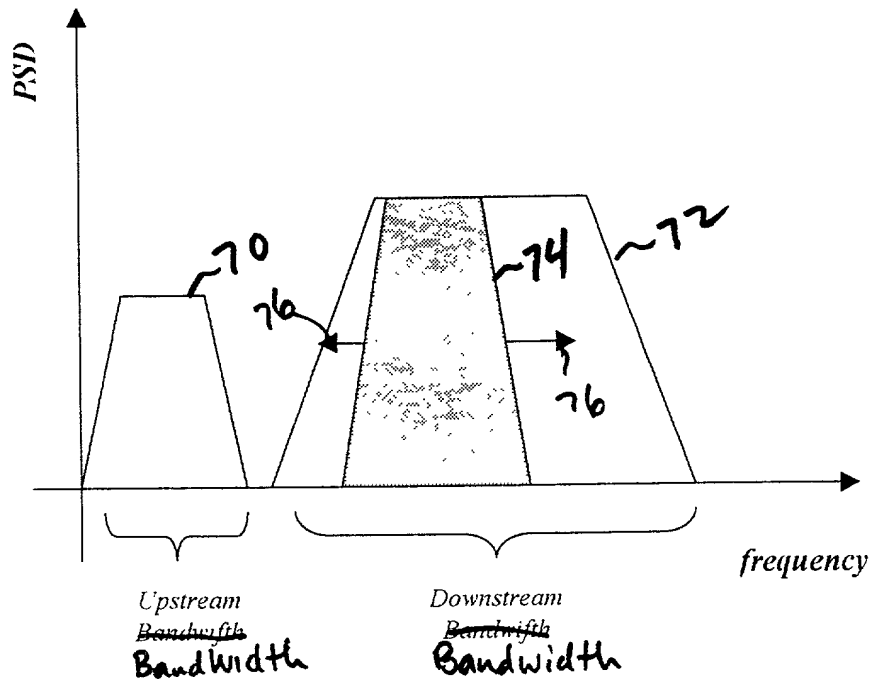


FIGURE 5. ATM IDLE CELL REMOVAL PROCEDURE.



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 FIGURE 7. MINIMIZATION OF NEXT NOISE FOR THE CPE RECEIVER
 BY SLIDING WINDOW METHOD. THE SHADED REGION IS THE NEEDED
 BANDWIDTH TO SUPPORT THE DOWNSTREAM BIT RATE.

TOP SECRET

25

80(1) →
Existing loop 1

80(2) →
Existing loop 2

80(M) →
Existing loop M

86 →
New initiated loop

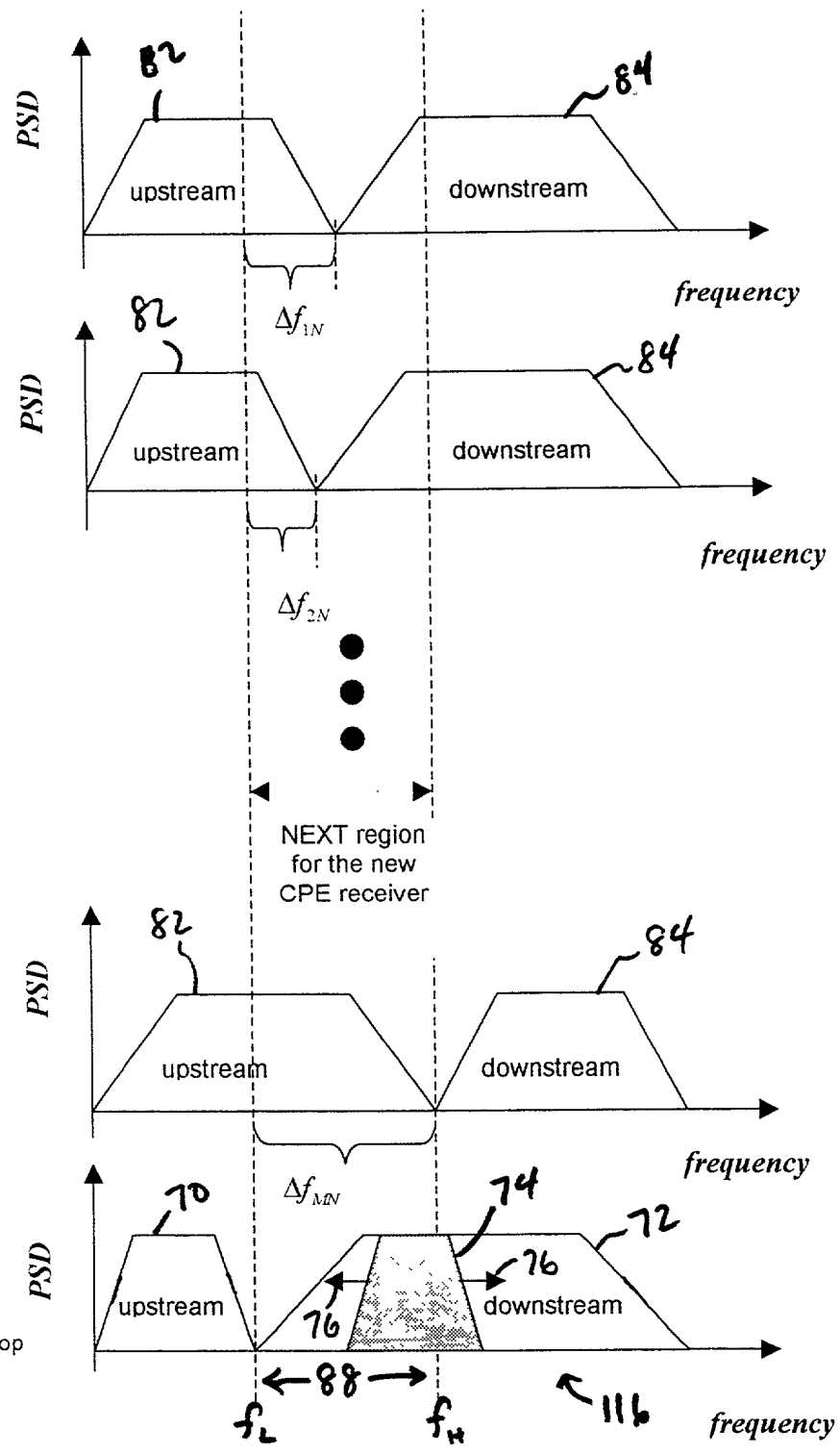


FIGURE 8 NON-OVERLAPPED SYSTEM.

26

120(1) ~

Existing loop 1

120(2) ~

Existing loop 2

10

TOGETHER "06T62001"

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120(M) ~

Existing loop M

126 ~

New initiated loop

20

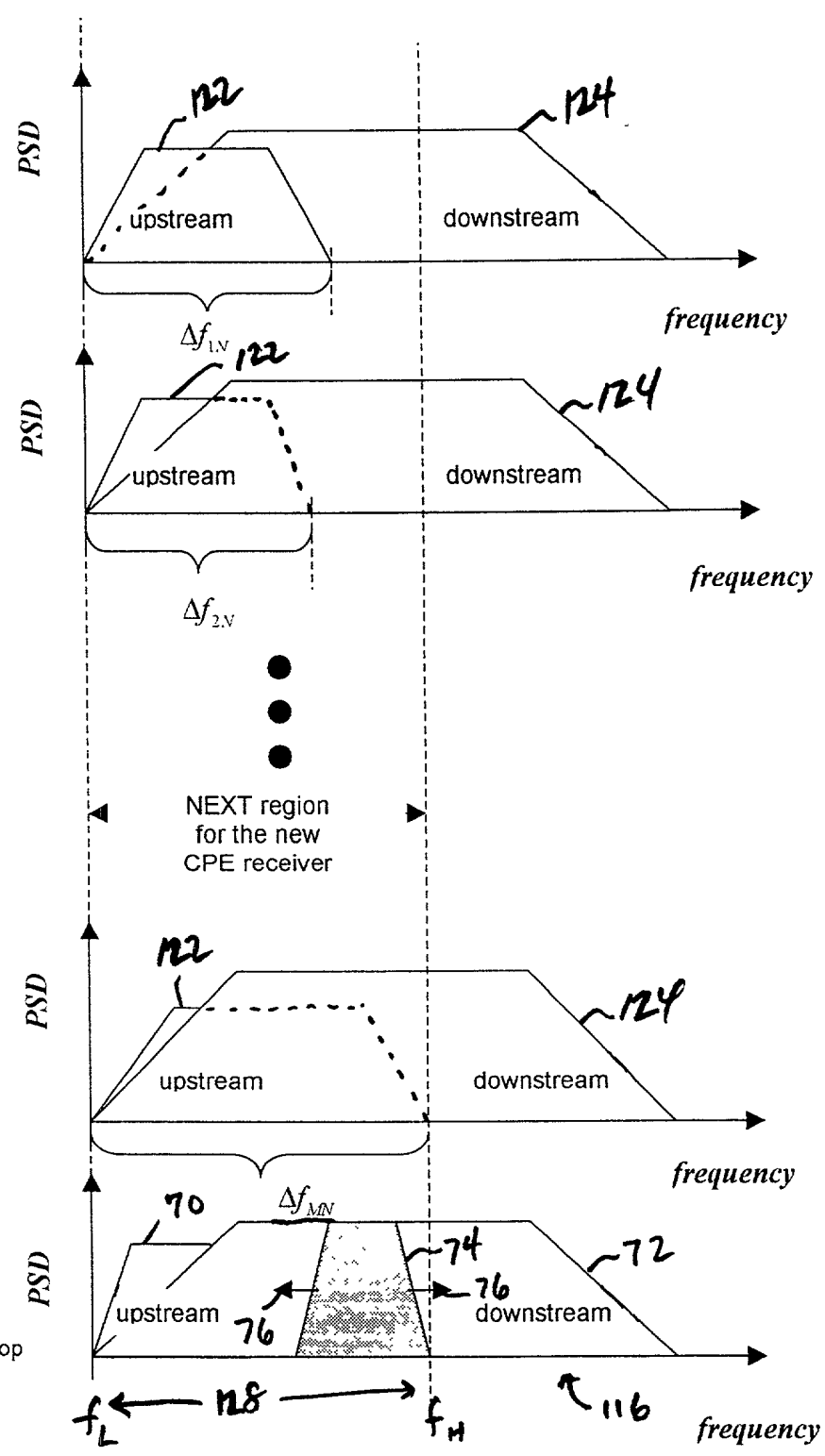


FIGURE 8. OVERLAPPED SYSTEM.

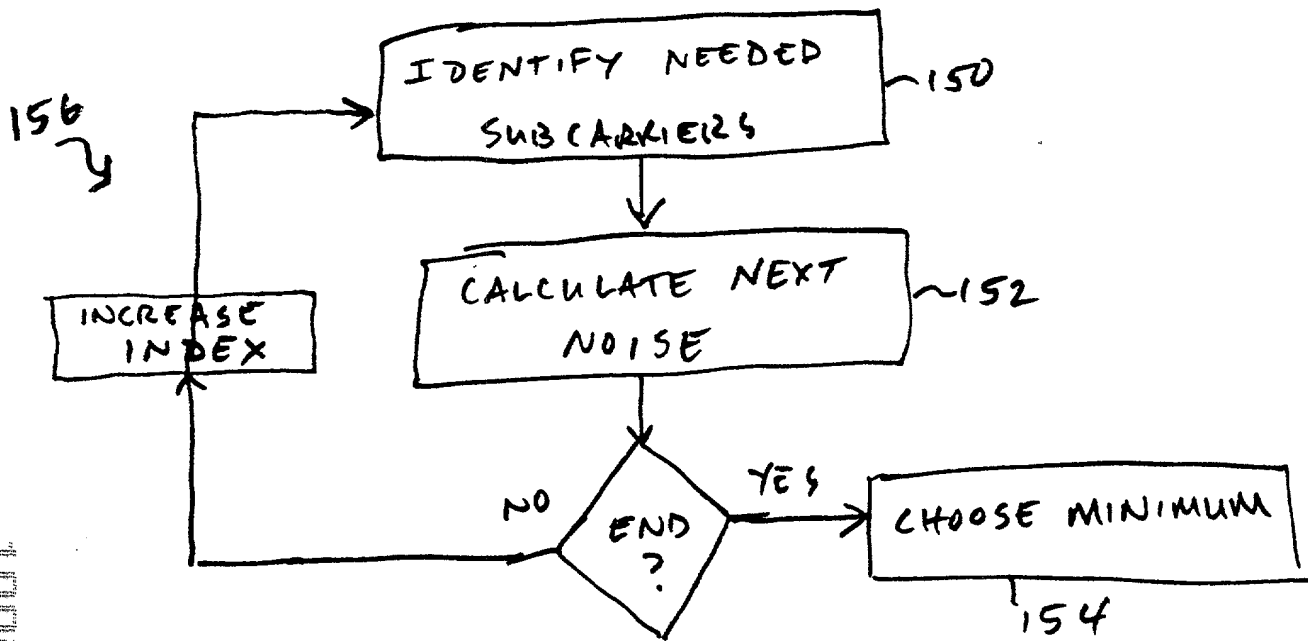


FIG. 10